

Registration Form

Name:

Rollno:

Email:

Mobile Number:

Signature

Participants

This Workshop is open
to all the 3rd years
and 4th years

Registration

No Registration Fee

CHEIF PATRONS

Sri. C.Gopal reddy
Chairman

Smt. C.Vasantha latha
Secretary

Dr.A.Raji Reddy
Director

Organised By

PATRON
Prof.G.Srikanth
HOD ECE

Dr.Sirisha
Convener

Sk.Dilshad
Co-convener

Members

Teaching and Non – teaching staff

Contact : Co-Convener
Sk.Dilshad : 9052009998

**“ONE DAY WORKSHOP
on Advanced VLSI Design
using Xilinx Vivado”**

27th Jan 2018



Organised by Department of
Electronics and communication
Engineering

SPEAKER:

K.Ganga Ravi

Principle Engineer IN SEMI

GOAIM SEMICONDUCTOR PVT. LMTD